

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

**Takashi OHSAWA**

Serial        10/621,357

No.:

Filed:        July 18, 2003

For:           SEMICONDUCTOR MEMORY  
                  DEVICE AND METHOD OF  
                  MANUFACTURING THE SAME

Atty. Docket No.: 002372.00044

Group Art Unit: 2824

Examiner:        Andrew Q. Tran

Confirmation     2565  
No.:

**LETTER ACCOMPANYING ISSUE FEE PAYMENT**  
**REGARDING INCORRECT PTA INDICATION**

U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop Issue Fee  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

Sir:

Applicant submits that the PTA of 201 days as indicated with the notice of allowance papers is incorrect, and requests that the PTA calculation be corrected. Rather, the PTA should have been indicated as 209 days. It is believed that the error may have been in connection with the time for response to the Office Action mailed May 1, 2006. A response to that Office Action was filed September 5, 2006, which should have resulted in a 35 day reduction. However, the USPTO incorrectly reduced the PTA by 43 days.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: May 18, 2007

By:           /Jordan N. Bodner/          

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